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1021.41260X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): A. KIKUGAWA, et al

Serial No.: 10/082,159

Filed: February 26, 2002

For: METHOD FOR TESTING OR RECORDING SERVO SIGNAL ON
PERPENDICULAR MAGNETIC RECORDING MEDIA

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98**

MS Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 6, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a first office action on the merits.

To the extent the documents listed on the attached form equivalent to Form PTO-1449 are not in the English language, the requirement of 37 CFR §1.98(a)(3) for a concise explanation of the relevance is satisfied by an English language version or translation of the foreign patent office report citing the documents.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (1021.41260X00) please credit any excess fees to such deposit account.

Respectfully submitted,



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FORM PTO-1449 U.S. Department of
Commerce (Rev. 4/92) Patent and Trademark
Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

1021.41260X00

SERIAL NO.

10/082,159

APPLICANT

A. KIKUGAWA, et al

FILING DATE

February 26, 2002

GROUP

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
													YES	NO
	2 0 0 1	1	5	5	3	2	2		6/01	Japan			XX	
	1	0	2	4	1	1	0	8	9/98	Japan			XX	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		H. Yamasaki, "Noise Technology in Electronic Circuits", p41 (3.3.4-Synchronized Addition), 1986.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])